



Docket No.: 8733.126.00-US  
(PATENT)

#26/EXT  
(3)  
10/29/02  
MS

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Duck-Kyun Choi

Application No.: 09/170,625

Group Art Unit: 2823

Filed: October 13, 1998

Examiner: G. Fourson

For: A METHOD FOR FABRICATING A THIN  
FILM TRANSISTOR

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TECHNOLOGY CENTER 2800

**REQUEST FOR EXTENSION OF TIME**

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

Applicant hereby petitions for a three month extension of time to and including October 19, 2002 to respond to the Office Action mailed April 19, 2002.

This petition is being filed in order to ensure copendency with application being filed concurrently herewith.

In the event that a further petition for an extension of time is required to be submitted at this time, applicant hereby petitions under 37 CFR 1.136(a) for an extension of time for as many months as are required to ensure that the above-identified application does not become abandoned.

Our check in the amount of \$920.00 covering the fee set forth in 37 CFR 1.17(a)(3) is enclosed. The Commissioner is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in

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this application by this firm) to our Deposit Account No. 50-0911, under Order No. 8733.126.00-US. A duplicate copy of this paper is enclosed.

Dated: October 16, 2002

Respectfully submitted,

By 

Song K. Jung

Registration No.: 35,210

William D. Titcomb

Registration No.: 46,463

MCKENNA LONG & ALDRIDGE LLP

1900 K Street, N.W.

Washington, DC 20006

(202) 496-7500

Attorneys for Applicant



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